FIG.1

ntin Ga As LAYER (EMITTER CAP LAYER)	10
n [†] GaAs LAYER (EMITTER CAP LAYER)	9
n Inx Aly Ga _{1-x-y} P LAYER (EMITTER LAYER)	-8
nInxAlyGa _{1-x-y} P LAYER (CONTROL LAYER)	7
p GaAs LAYER (BASE LAYER)	-6
n GaAs LAYER (COLLECTOR LAYER)	-5
n [†] GaAs LAYER (CONDUCTIVE LAYER)	-4
BUFFER LAYER	-3
Go As SUBSTRATE	-2

FIG.2

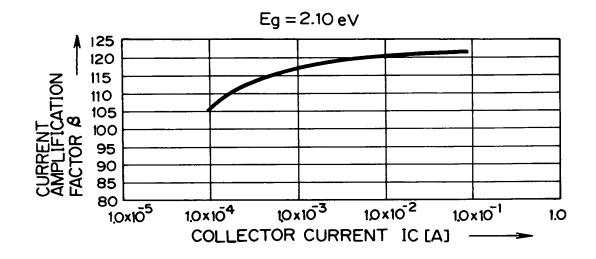


FIG.3

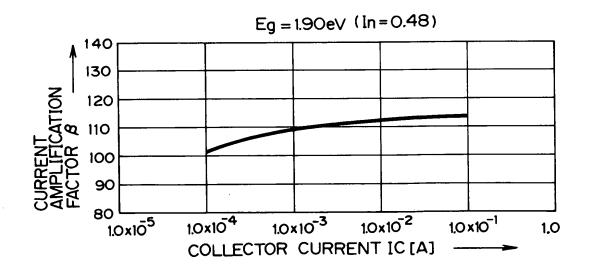


FIG.4

